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LSI Logic Corporation  
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1621 Barber Lane, MS D-106  
Milpitas, CA 95035Date October 28, 2005Number of pages including cover sheet 2

To: US PTO

Mail Stop Status Request

TC 2188

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From: Connie del Castillo

Intellectual Property Paralegal

Telephone (408) 433-7191  
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## REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please commentApplication Number: 10/780,971  
Filing date: February 18, 2004  
First named inventor: Pareesh Chatterjee et al.  
Attorney docket number: 03-2043

Transmitted herewith for filing via facsimile:

- Status Request Letter

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Signature Connie del Castillo

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OCT 28 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of :**

Paresh Chatterjee et. al.

**Serial No. :**

10/780,971

**Filed :**

February 18, 2004

**For :**

# Systems And Methods For Cache Synchronization Between Redundant Storage Controllers

www.pearsoned.com

**Group Art Unit: 2188**

**Examiner :**

**Atty Docket :** / 03-2043

## STATUS REQUEST

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

Respectfully submitted,

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Date: 27 Oct 05

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